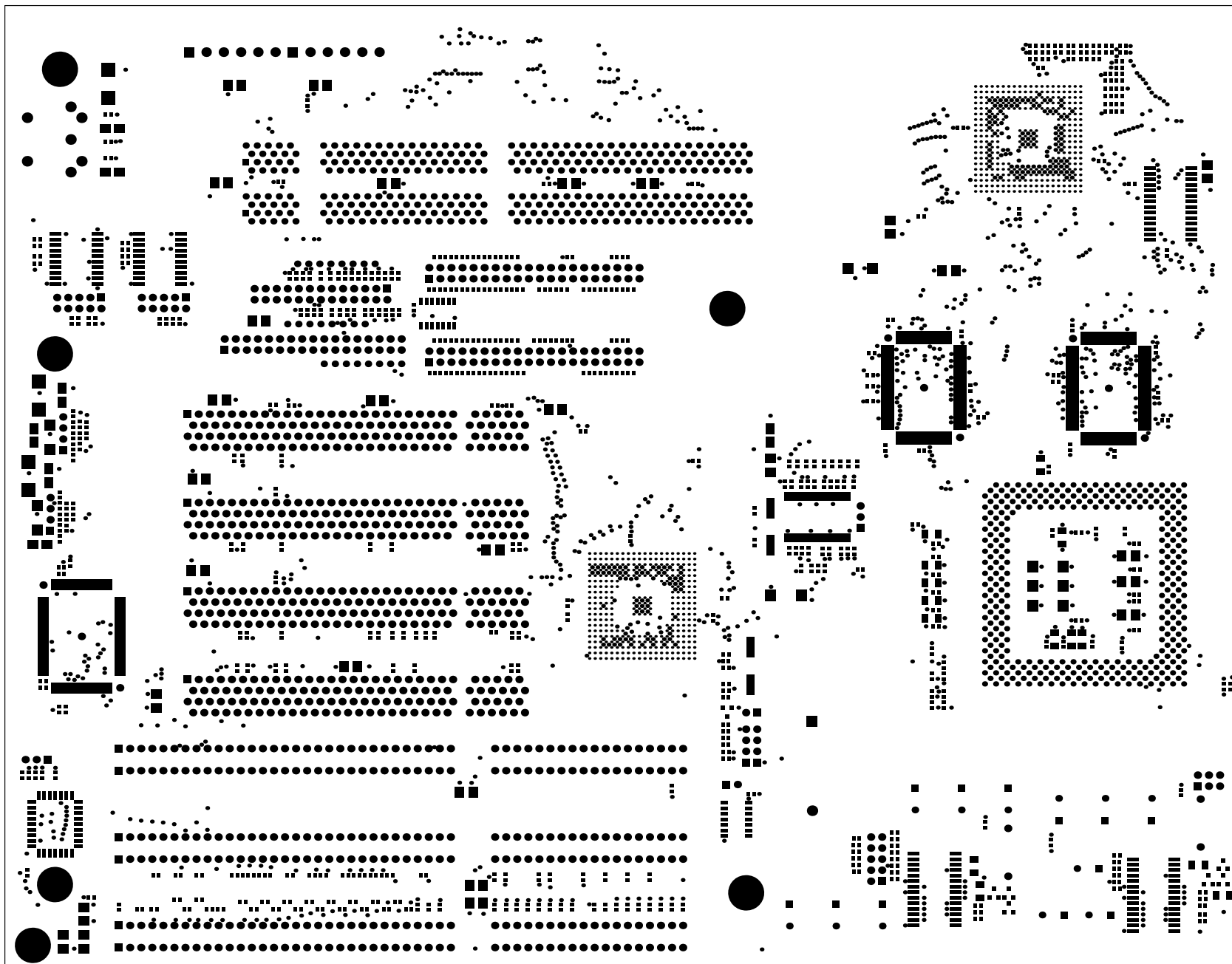
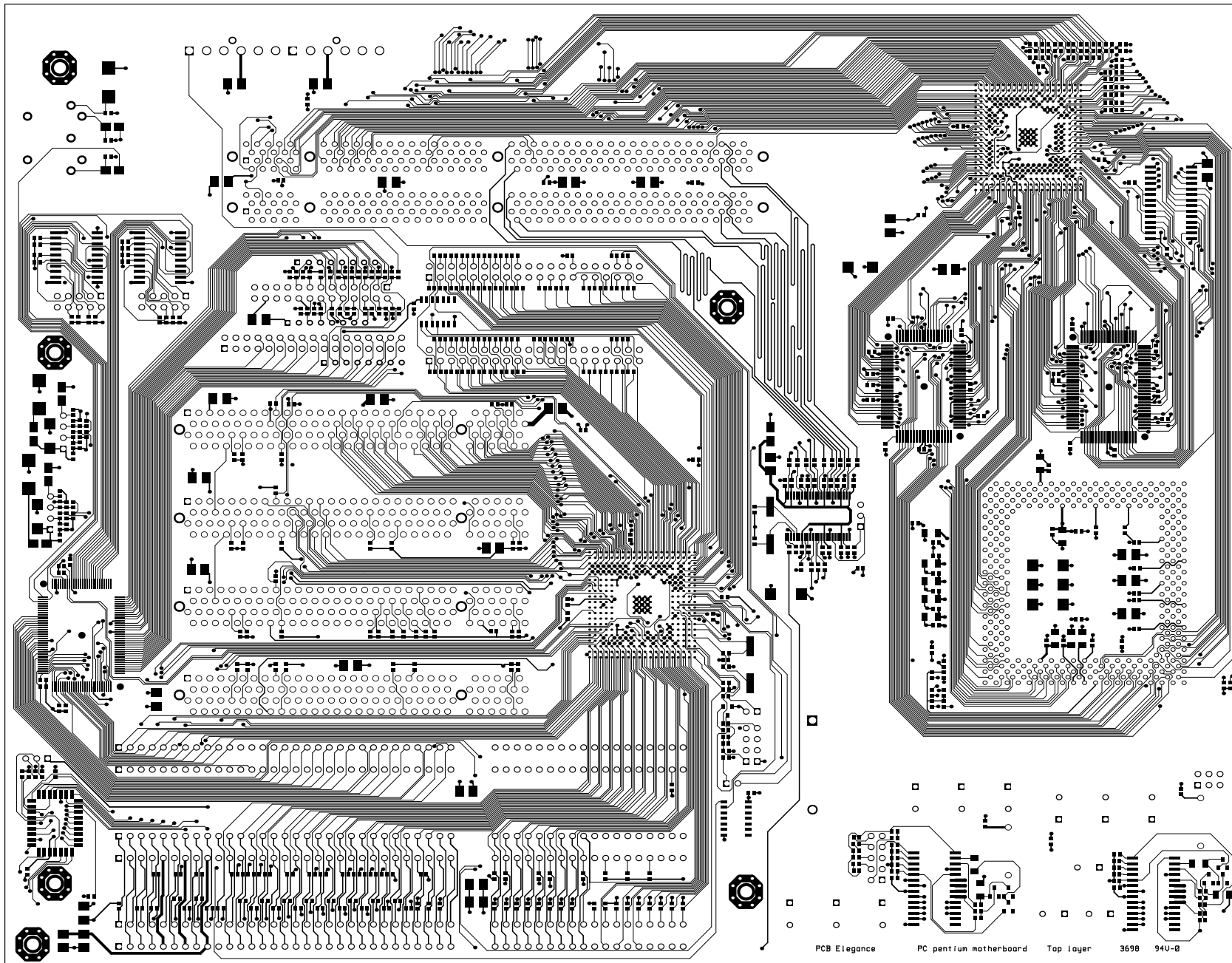


PcMotherBoard
Paste mask top
Apr 29, 2012 14:18:57

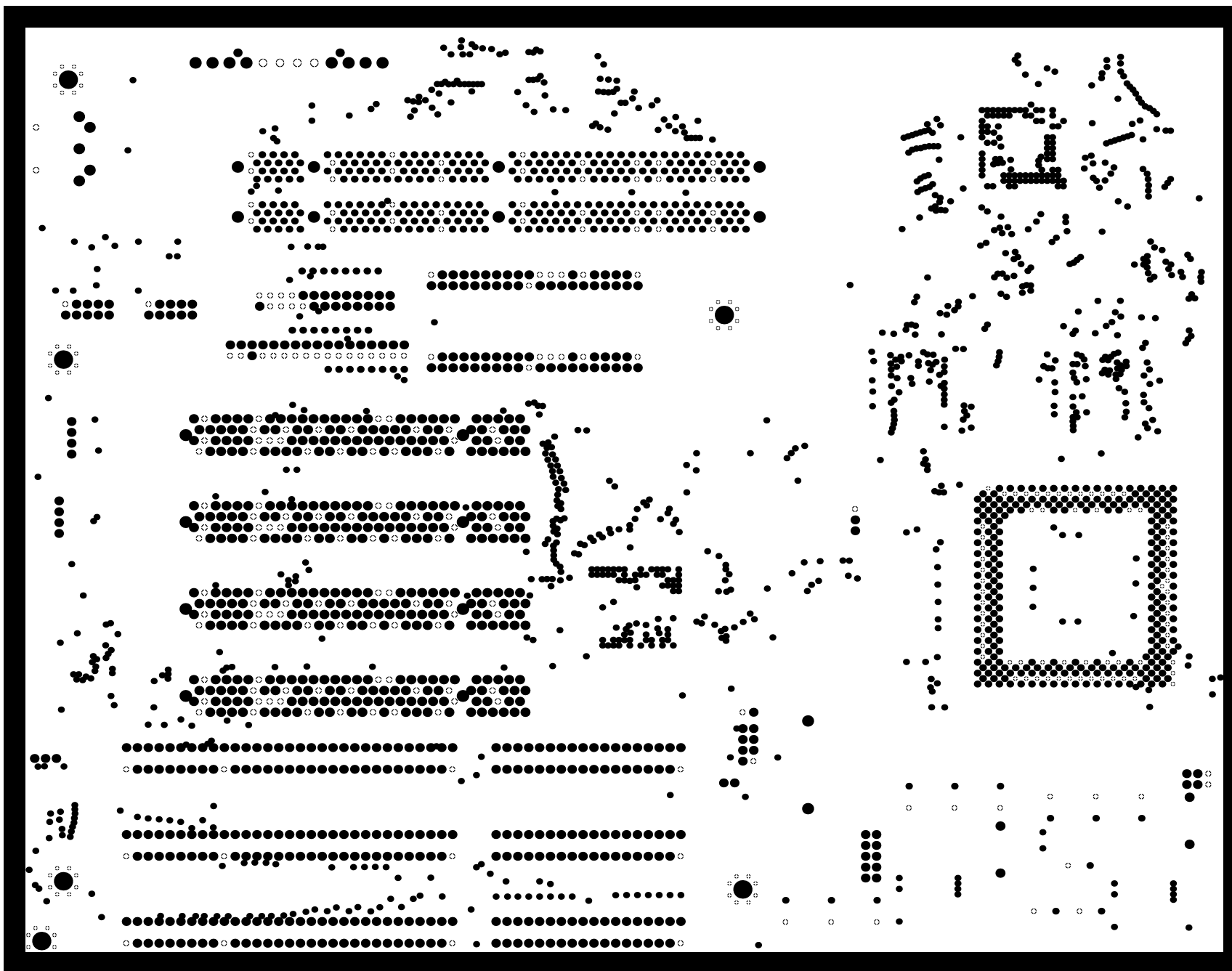


PcMotherBoard
Solder mask top
Apr 29, 2012 14:18:57

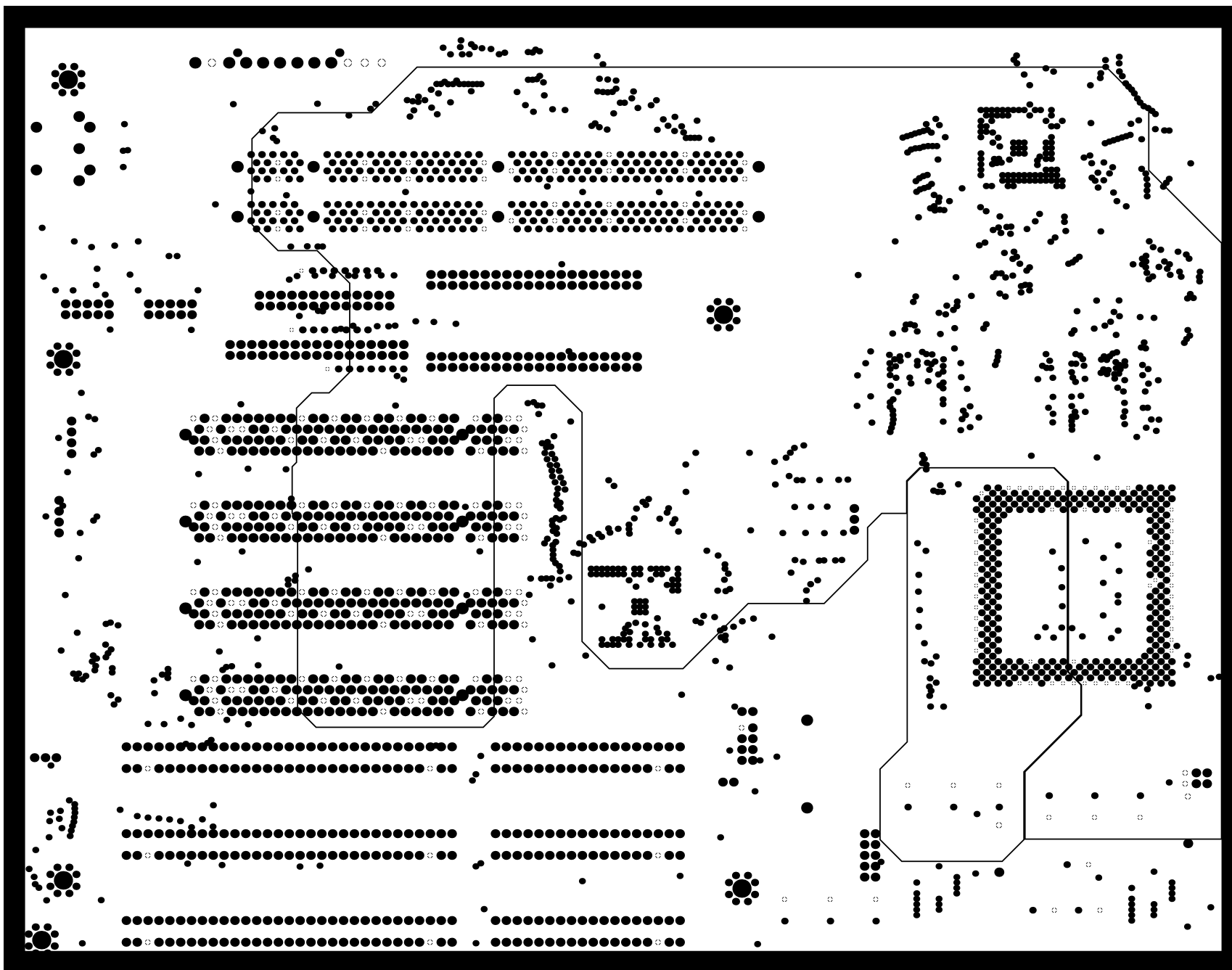


PcMotherBoard
Layer 1 (Top)
Apr 29, 2012

14:18:57



PcMotherBoard
Layer 2
Apr 29, 2012 14:18:57

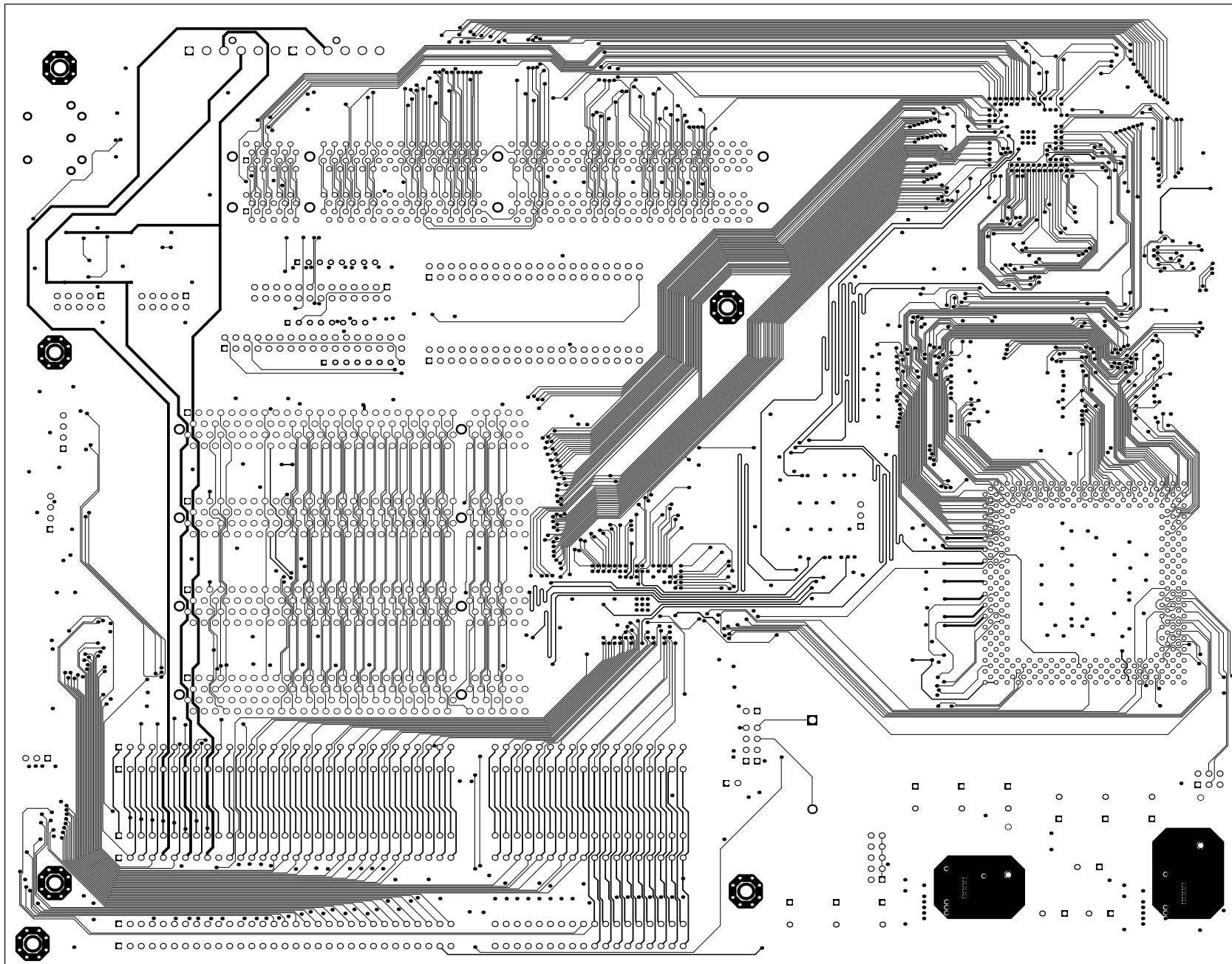


PcMotherBoard

Layer 3

Apr 29, 2012

14:18:57



PcMotherBoard
Layer 4 (Bottom)
Apr 29, 2012 14:18:57



PcMotherBoard
Routing keepout top 0
Apr 29, 2012 14:18:58

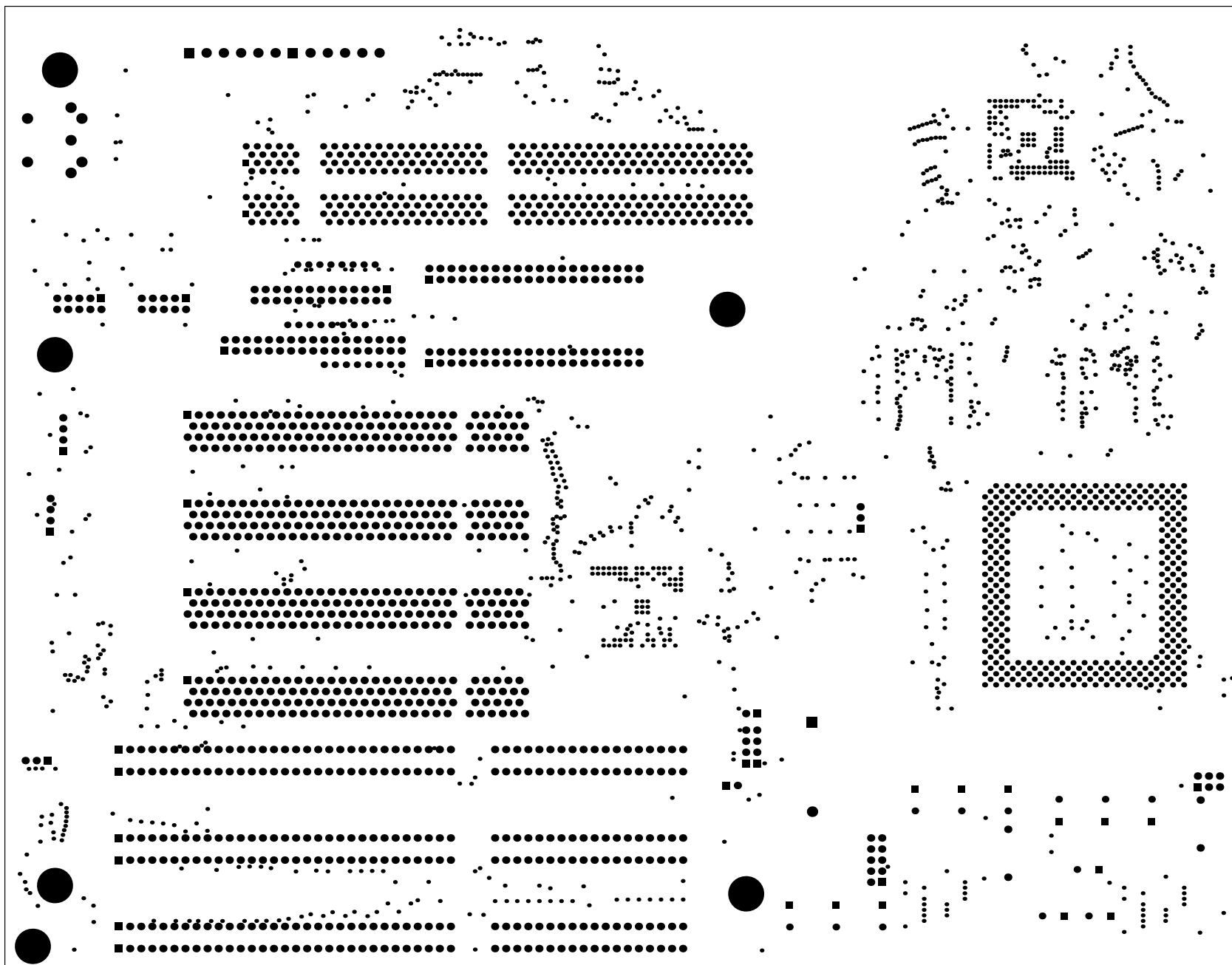


PcMotherBoard
Routing keepout inner 1
Apr 29, 2012 14:18:58

PcMotherBoard
Routing keepout inner 2
Apr 29, 2012 14:18:58



PcMotherBoard
Routing keepout bottom 3
Apr 29, 2012 14:18:58



PcMotherBoard
Solder mask bottom
Apr 29, 2012 14:18:58



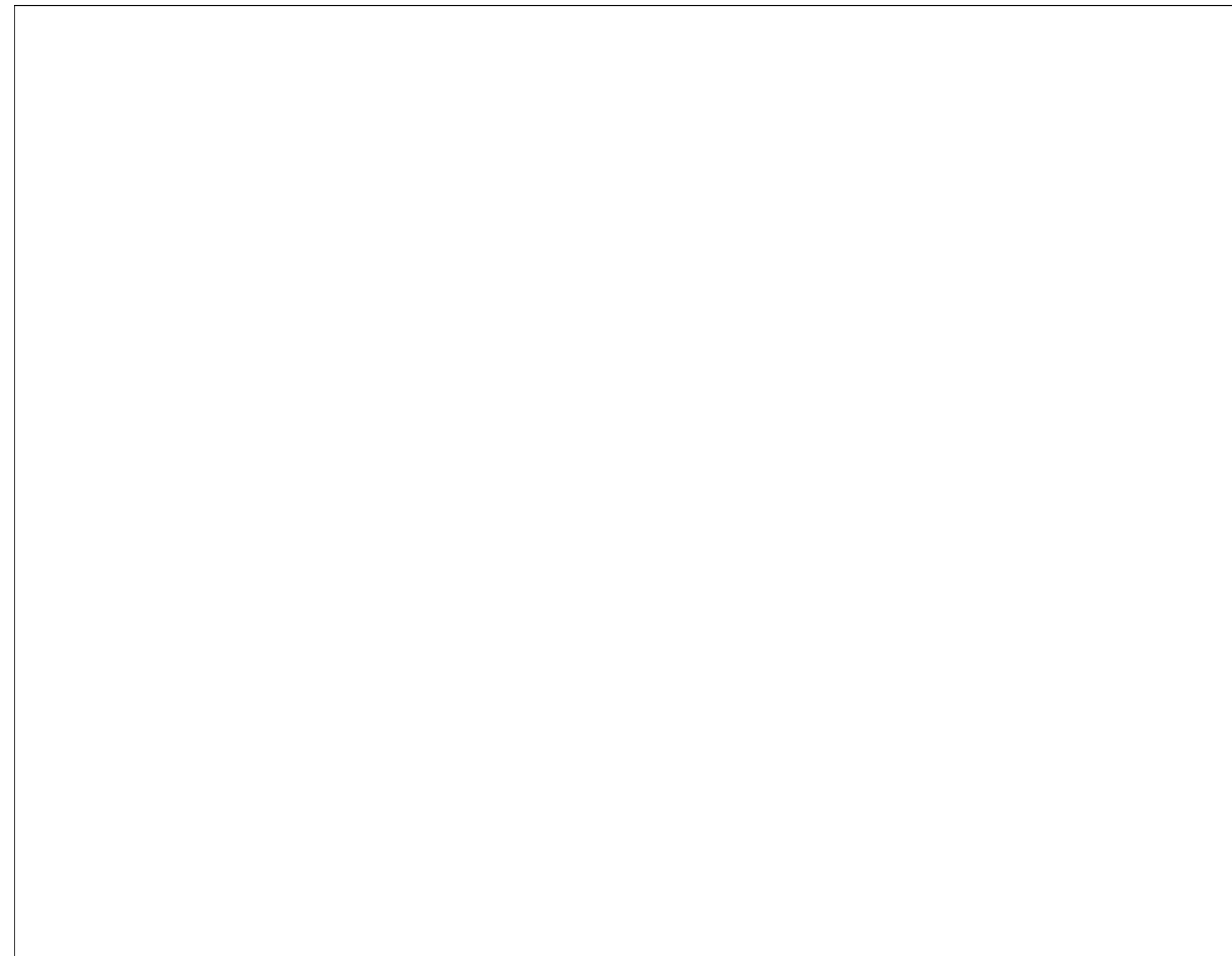
PcMotherBoard
Paste mask bottom
Apr 29, 2012 14:18:58



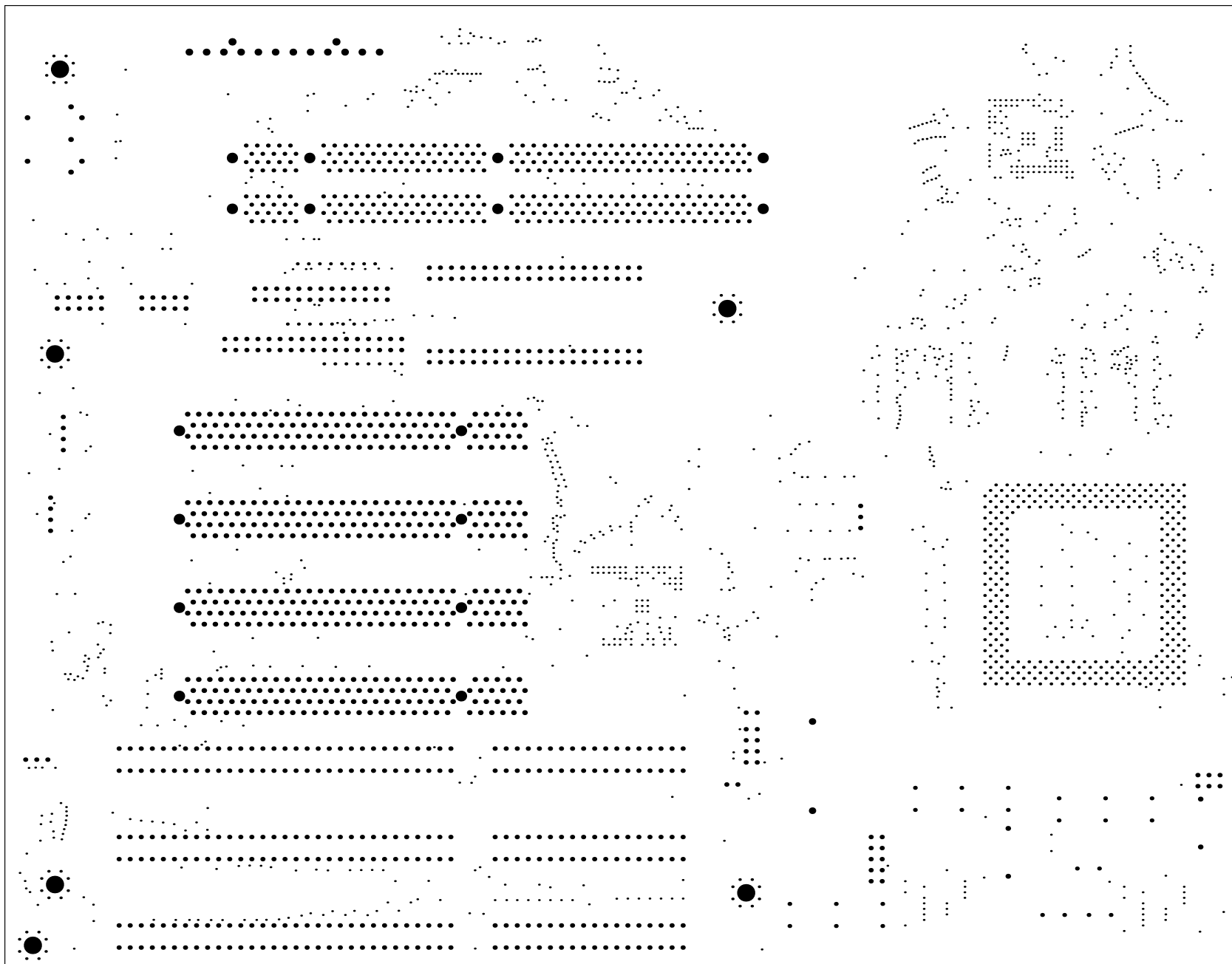
PcMotherBoard
Silkscreen bottom (refs)
Apr 29, 2012 14:18:58



PcMotherBoard
Silkscreen bottom (values)
Apr 29, 2012 14:18:58

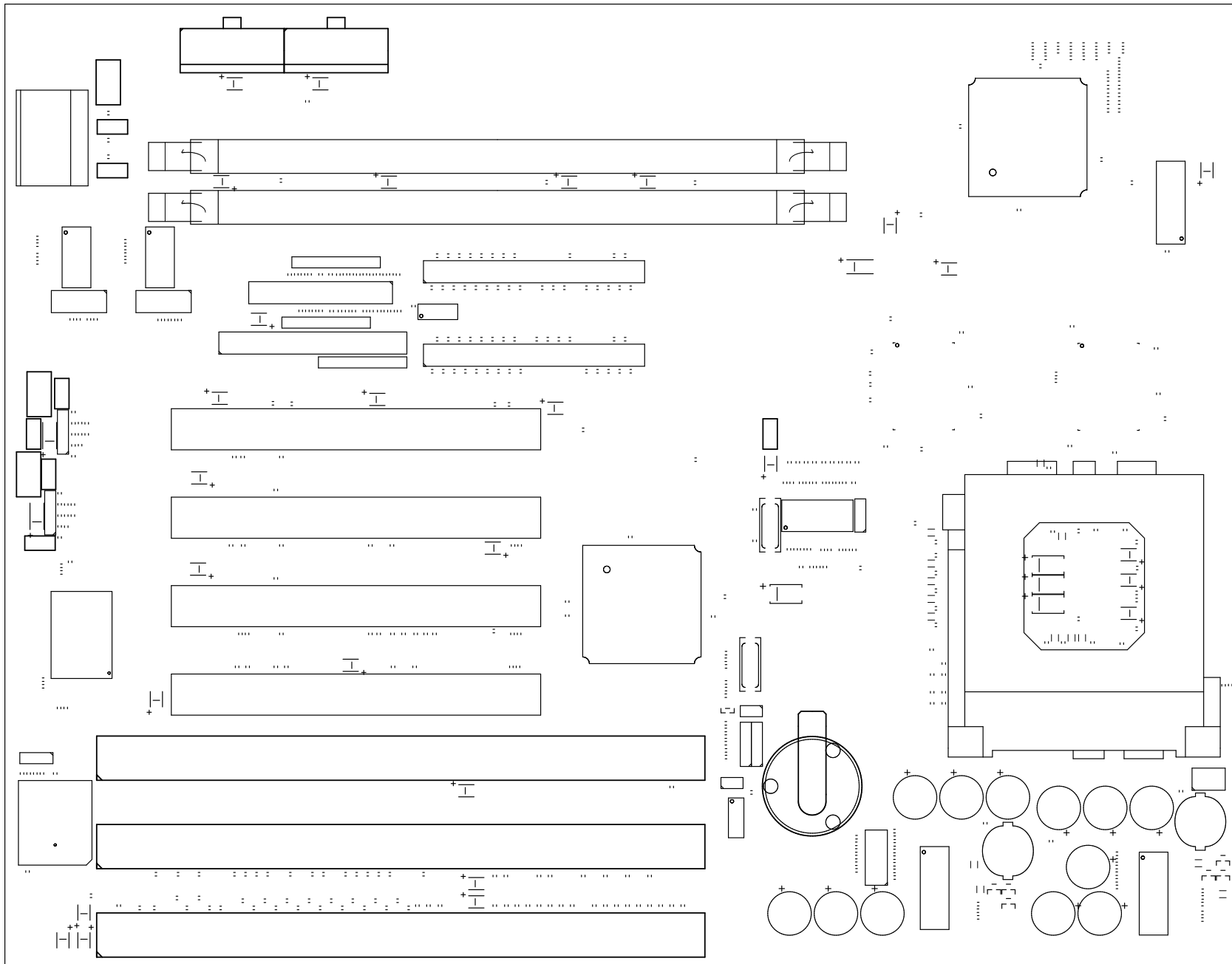


PcMotherBoard
Silkscreen bottom
Apr 29, 2012 14:18:58



PcMotherBoard
Drills
Apr 29, 2012 14:18:58





PcMotherBoard
Component outline top
Apr 29, 2012 14:18:58



PcMotherBoard
Component outline bottom
Apr 29, 2012 14:18:58



PcMotherBoard

Info1

Apr 29, 2012

14:18:58



PcMotherBoard
Info2

Apr 29, 2012 14:18:58



PcMotherBoard

Info3

Apr 29, 2012

14:18:58



PcMotherBoard

Info4

Apr 29, 2012

14:18:58